Diamond-like carbon decoration enhances the field electron emission of silicon nanowires

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**Abstract**

Diamond-like carbon decorated silicon nanowires (DLC-SiNWs) have been fabricated using magnetic filtered vacuum plasma deposition process at room temperature on SiNW array substrates. DLC-SiNWs are composed of uniformly distributed amorphous carbon and a few diamond nano-grains, in which there are more than 70% sp³ bonds in the carbon film. The turn-on field (E_on) of DLC-SiNWs changes with the thickness increase of DLC layer. E_on of DLC-SiNWs with a thickness of 60 nm reaches at a lowest value, i.e. 4.36 V/μm, which is better than the original SiNWs. These indicate that the field emission (FE) property of SiNWs decorated with a suitable thickness DLC is enhanced due to the work function reducing of the tip of SiNWs, at which the amorphous carbon phase layer embedded with a few diamond nano-particles has been decorated.

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1. Introduction

Dating from the discovery of carbon nanotubes (CNTs) by Iijima in 1991 [1], many researchers have paid their attention to the study of one-dimensional nanostructures due to their promising properties, especially the FE property applied to the flat-panel displays, such as carbon nanotubes and semiconductor nanowires [2]. With the development of micromachining and integration techniques, SiNWs, firstly fabricated in 1964 [3], exhibit good FE property through the effort made by the researchers. Huq et al. [4] and Zhao et al. [5] fabricated the ultra sharp silicon field emitters, which had relatively high and stable emission current. Zeng et al. [6] improved the field emission property of the SiNWs fabricated by CVD method by post-annealing the samples at a high temperature for 24 h. Many other means, such as doping [7], and catalyst etching [8], to improve the FE property of SiNWs have also been examined. On the other hand, modification with sputtering thin film is an efficient approach to the development of the FE of SiNWs [9], and DLC thin films with negative electron affinity present better FE property than that of a diamond film and silicon tips [10]. In this paper, the fabrication and FE characteristics of DLC-SiNWs have been investigated.

2. Experiment

A relatively simple chemical etching approach is utilized here to synthesize SiNWs [11,12]. N type (100) silicon wafer is used as the substrate and cut into regular shapes. After cleaning ultrasonically in turn in acetone and ethanol for 5 min to degrease, the silicon slices were transferred into 1% diluted HF solution to remove the oxide layer. Each procedure ended with rinsing the samples under flowing deionized water and drying naturally at room temperature. After cleaning, the silicon slices are put into the solution of AgNO₃/HF, whose concentrations are 0.01 mol/L and 8%, respectively, for 1 min to synthesize silver catalyst particles, and then would be quickly immersed into H₂O₂/HF solution to carry out the chemical etching process for 1 h when SiNWs are gradually fabricated. In order to decrease the influence of SiO₂ layer covered on SiNWs, the as-grown SiNWs samples are treated in HF aqueous solution to remove the SiO₂ layer existing on SiNWs before DLC film deposition. DLC layers are prepared on SiNWs substrates at room temperature by the magnetic filtered vacuum plasma deposition (MFVDP) process during which the carbon plasma is produced with high purity by an arc source with a filter. The operation current of the arc source is about 100 A and a bent magnetic filter duct is used to remove the unwanted large particles. The base pressure of the deposition chamber is maintained below 2×10⁻⁶ Pa. The thickness of DLC layers is selected from 10 to 100 nm.

Scanning electron microscope (SEM S-4800, Hitachi) and transmission electron microscope (FEI TECNAI F30, PHILIPS) were employed to characterize the morphology and structure of SiNWs. X-ray photoelectron spectroscopy (XPS) and Raman spectroscopy were used to determine the chemical state of coating. Field electron emission measurement was carried out in a diode system whose base pressure was below 3×10⁻⁷ Pa. The distance between the anode (diameter of the polished anode plate is about 50 mm) and cathode (to be ground) was about 500 μm, and controlled by an electric manipulator which
was a drive engine connected to a computer and could control the movement of the anode with the accuracy of 1 μm. During the measurement of the field emission, the working voltage varying from 0 to 10 kV was applied, and the sample size is about 3 × 3 mm². The measurement data were automatically recorded by a computer connected to the measurement system, and analyzed by the Fowler–Nordheim (F–N) equation.

3. Results and discussion

Fig. 1(c) shows that well aligned SiNWs about 10 μm in length are uniformly formed on the Si substrates by the chemical etching process. Fig. 1(a) and (b) reveals that the SiNWs are about 100 nm in diameter and are [100] oriented. DLC layers with different thicknesses are deposited using MFVPD process on SiNW substrate. Morphology of DLC-SiNWs could change with thickness of the layer, as shown in Fig. 2. Non-uniform structures are formed on the DLC layer with a lower thickness, and only discrete and sparse carbon particles are observed. The uniformity of DLC-SiNWs gradually increases with the thickness of layer. Thus continuous layer can be formed when the thickness of the DLC layer is larger than 20 nm. Fig. 3 shows the morphology of the DLC layer with the thickness of 60 nm decorated on the surface of SiNWs, where Fig. 3(a), (b) and (c) are HRTEM and SEM images of DLC-SiNWs, respectively. The continuous DLC layer decorated on the top surface of SiNWs has been observed. For the DLC layer fabricated using MFVPD process at room temperature, the J of SiNWs increases to 294 μA/cm² at the applied field of 5.94 V/μm. Comparing with the reports in which the J of SiNWs was usually in the range from 8.6 to 15 V/μm [17], the J of SiNWs fabricated by the chemical etching process is lower. DLC is a negative affinity material with high FE property. The application of DLC layer deposition on the top surface of SiNWs can improve the FE of emitters. Due to DLC layer deposition on the top surface of SiNWs, the J of SiNWs increases to 294 μA/cm² at the applied field of 5.94 V/μm for 60 nm DLC layer, which is two times higher than that of the original SiNWs. At the same time, the minimum Eon, 4.36 V/μm, can be obtained after the DLC layer was deposited on the top surface of SiNWs. Compared with original SiNWs, the Eon of DLC-SiNWs is reduced by 13%. It is indicated that the DLC layer deposition has enhanced the FE of SiNWs.

Fig. 1. HRTEM (a), TEM (b) and SEM (c) images of the SiNWs fabricated by using chemical etching at room temperature.
Field emission characteristics of DLC-SiNWs are analyzed by using the Fowler–Nordheim (F–N) equation given below [18]

\[
\ln \left( \frac{J}{E^2} \right) = \ln (ra \varphi^{-1} \beta^2) - \frac{sb \varphi^{3/2}}{\beta} - \frac{ra \varphi^{-1} \beta^2}{E}
\]

(1)

where \( \varphi \) is the work function, \( \beta \) is the field enhancement factor, \( a \) and \( b \) are the F–N constants, \( r \) and \( s \) are appropriate values of the intercept and slope correction factors, respectively. F–N plot is expected to be a good straight line because of small variation of \( r \) and \( s \) with \( 1/E \). Further to simplify the equation above, \( r \) multiplied by \( a \) equals a constant \( 1.54 \times 10^{-6} \) A (eV) V\(^{-2} \) and \( s \) multiplied by \( b \) equals a constant \( 6.83 \times 10^3 \) (eV)\(^{-3/2} \) V (\( \mu m \))\(^{-1} \). According to the Fowler–Nordheim (F–N) equation, we define a parameter \( K \), which associates with the structures (composition, tip sharpness, aspect ratio, etc.) and electrical parameters (conductivity, work function, etc.) of emitters and indicates an ability of field electron emission from materials, and is given as:

\[
K = B = - \frac{sb}{S}
\]

(2)

where, \( S \) is a slope of Fowler–Nordheim plots. From Eq. (2), it can be seen that large \( \beta \) and low \( \varphi \) in the emitter make the parameter \( K \) increase, and show the high ability of field electron emission for the emitter. As shown in Fig. 5(b), it has been observed that all the F–N plots are approximate lines in the high-current density areas and fitted with the F–N equation, which suggests that the electrons are emitted by the cold FE process. The \( K \) of DLC-SiNWs can be calculated by the F–N plots and Eq. (2), and listed in Table 1. Obvious increase of the \( K \) for DLC-SiNWs has been observed due to the DLC deposition, and the \( K \) reaches at a maximum value, i.e. 148.98, when 60 nm DLC layer are deposited on SiNWs. This indicates that a suitable deposition
thickness of DLC layer could enhance the field emission properties of SiNWs.

During the DLC layer deposition, the SiNW diameter increases with the ever-increasing DLC thickness. \( \beta \) is a parameter describing the aspect ratio of SiNWs, and it decreases with the increase of SiNW diameter, which deteriorates the field emission of the DLC-SiNW hybrids. However, DLC is a negative electron affinity material, which can greatly facilitate electrons tunneling barriers. Additionally, DLC is full of defects, especially vacancy-related defects, which are supposed to increase the field electron transmission traces, i.e., increase the number of effective emission sites during field electron emission and therefore improve the field emission of DLC-SiNW hybrids [19]. That’s to say, both the SiNW diameter and the number of emission sites influence the \( \beta \), but in the defect-rich DLC decorated SiNWs, the later is dominative. Furthermore, the increased state density of defects ascends the Fermi Lever, and thus decreases the work function of the DLC-SiNW hybrids [20]. In a word, the concurrent effect of emission site increase induced promotion of \( \beta \) and the decrease of work function improve the field emission of DLC-SiNW hybrids within a certain DLC thickness, which is ~60 nm in our study. However, with further increase of the DLC thickness, the SiNW diameter induced \( \beta \) decrease is dominative, and therefore deteriorates the field emission of DLC-SiNW hybrids.

4. Conclusions

In summary, DLC-SiNWs with uniform distribution have been fabricated using MFCPD process at room temperature on a well aligned SiNW array substrate. Raman spectra and XPS analysis prove that there are more than 70\% sp\(^3\) bond existing in the DLC layer. Due to the coupling interaction between DLC nanolayer and SiNWs making the electrons much easier to emit from the tip of the emitter under the applied field, and reducing the \( \Phi \) of the emitter materials, the decoration of DLC layer with an appropriate thickness at the tip of SiNWs can induce the \( K \) of the emitter increasing from 99.38 to 148.98, and enhance the FE property of SiNWs. The lowest \( E_{\text{on}} \) of the DLC layer decorated is about 4.36 V/\( \mu \)m and the \( J \) can reach 294 \( \mu \)A/cm\(^2\) at the applied field of 5.94 V/\( \mu \)m. The FE property of DLC-SiNWs is better than that of SiNWs.

Table 1

<table>
<thead>
<tr>
<th>SiNWs</th>
<th>20 nm decorated</th>
<th>60 nm decorated</th>
<th>100 nm decorated</th>
</tr>
</thead>
<tbody>
<tr>
<td>( J (\mu A/cm^2)^a )</td>
<td>101</td>
<td>223</td>
<td>294</td>
</tr>
<tr>
<td>( E_{\text{on}} (V/\mu m)^b )</td>
<td>5.01</td>
<td>4.59</td>
<td>4.36</td>
</tr>
<tr>
<td>( K = (\frac{\beta}{\Phi^{3/2}})^c )</td>
<td>99.38</td>
<td>126.54</td>
<td>148.98</td>
</tr>
</tbody>
</table>

\( a \) \( J \) is the field emission current density at applied field of 5.94 V/\( \mu \)m.

\( b \) \( E_{\text{on}} \) is the turn-on field obtained from J–E curves.

\( c \) \( K \) is calculated by using the F–N plots and Eq. (2).
Acknowledgments

This work is supported by the National Basic Research Program of China (No: 2010CB832905), and partially by the National Natural Science Foundation of China (No: 10575011) and the Key Scientific and Technological Project of Ministry of Education of China (No: 108124).

References